CALL FOR PAPERS

2025 10th International Conference on Signal Processing and Communication (ICSC 2025)
February 20-22, 2025
Jaypee Institute of Information Technology, NOIDA, India
IEEE Conference Record No. : 64553
Conference will be held in HYBRID Mode

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About the Conference:

ICSC 2025 is 10th in the series of International Conference on Signal Processing and Communication (ICSC) to be held at JIIT, Noida. The last nine conferences in the series were highly successful and flagship event at JIIT, Noida. These conferences were attended by several international and national delegates. The conferences also witnessed exceptional technical talks and debates in the area of Signal Processing, Communication, VLSI, Intelligent Computing and related areas. The goal of ICSC series conferences is to provide an exceptional platform for researchers, academicians, industry and government practitioners to discuss and showcase their recent contributions in the form of technical papers, expert talks and poster presentations in the area of Electronics and Communication Engineering.

ICSC 2025 will deal with research areas that broadly specify Communication, Signal Processing, VLSI, Intelligent Computing and Machine Learning. Tutorials and keynote sessions have been arranged to benefit the researchers working on recent challenges in these emerging technical areas. All the submitted papers will undergo a peer review process by experts on the subject and if accepted, will be assigned to an appropriate session for presentations. The conference will be held in **HYBRID** Mode. (http://www.jiit.ac.in/jiit/ICSC/)

Paper Submission:

ICSC 2025 invites authors to submit their original research and findings in the form of technical paper for the single blind peer review process. Prospective authors are requested to submit original papers that have not been published or submitted for publication elsewhere. Paper submission can be done through EDAS using following link: https://edas.info/N32576. Papers should strictly adhere to the IEEE Conference formatting guidelines using the templates available at https://www.ieee.org/conferences/publishing/templates.html. The conference paper should not exceed 6 pages including references. In accordance with the IEEE policy, plagiarism in any form, at any level, is unacceptable. IEEE policy on plagiarism can be found at https://www.ieee.org/publications/rights/plagiarism/plagiarism.html. Authors are requested to carefully upload the details of co-authors while uploading the paper on EDAS portal. No author will be included or excluded at the later stage.

Papers on the following Tracks are invited:

Track-1: Communication
Track-2: Signal Processing
Track-3: VLSI Technology & Embedded Systems
Track-4: Intelligent Computing and Machine Learning

Specific topics under the tracks can be found at : https://www.jiit.ac.in/jiit/ICSC/scope.php

Important Dates:

Full Paper Submission :   **25th October, 2024**
Notification of Acceptance: **10th December, 2024**
Final Paper Submission :   **25th December, 2024**

Presentation and Registration Requirements:

It is required that at least one of the authors of the accepted paper must register for the conference and should be available for the paper presentation at the conference. Any paper that is not presented by an author of the paper, will be withdrawn from the proceedings and thus from IEEE Xplore.

	Indian Participants		Foreign Participants	
Authors	**IEEE Members**	**Non IEEE Members**	**IEEE Members**	**Non IEEE Members**
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Host Location: Jaypee Institute of Information Technology, A-10, Sector-62, Noida, India (http://www.jiit.ac.in/)
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